

NOTICE ON CHIPS

CASINO CONTROL (GAMING EQUIPMENT) REGULATIONS 2009

1. INTRODUCTION

- 1.1 This notice is issued by the Casino Regulatory Authority of Singapore (the “Authority”) pursuant to regulation 20(4) of the Casino Control (Gaming Equipment) Regulations 2009.
- 1.2 This notice shall take effect from 14 October 2009.
- 1.3 All terms used in this notice, unless the context otherwise requires or otherwise defined, shall have the same meanings as in the Casino Control Act (Cap. 33A) (the “Act”) and any regulations made thereunder.

2. UNDERLYING PRINCIPLES

- 2.1 The evaluation of gaming equipment serves to ensure that the operation of a casino is and remains free from criminal influence or exploitation and that gaming in a casino is conducted honestly.
- 2.2 Hence, all gaming equipment must be designed and manufactured to allow operations of a casino to be conducted in a manner that is honest, secure, auditable and reliable.

3. REQUIREMENTS FOR CHIPS

- 3.1 Chips shall be designed such that counterfeiting of the chips is prevented to the greatest extent reasonably possible.
- 3.2 Chips shall be designed such that chips of different denominations and uses (e.g. premium chips, different players’ roulette chips, promotional chips, souvenir chips, etc) are readily distinguishable.
- 3.3 Chips shall be designed such that the chips, even when stacked together, are identifiable to casino employees (including when viewed via any surveillance system of a casino operator).
- 3.4 Proposed gaming chip designs shall first be submitted to the Authority for preliminary evaluation before production commences. The Authority reserves the right to review all actual production gaming chips to ensure compliance

with the initial proposed design before granting approval in accordance with the Act and any regulations made thereunder.

- 3.5 There shall be sufficient security measures, internal controls and quality verification criteria in place during the manufacturing, quality control, packing, storage, delivery and destruction of the chips